

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	)	
Min-hwa Chi	)	Examiner: To be assigned
	)	
Serial No. Not yet assigned	)	Art Unit: To be assigned
	)	
Filed: Concurrently herewith	)	
	)	
For: TRANSISTOR AND LOGIC CIRCUIT	)	
ON THIN SILICON-ON-INSULATOR	)	
WAFERS BASED ON GATE INDUCED	)	
DRAIN LEAKAGE CURRENTS	)	
	)	

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Mail Stop Patent Application  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT UNDER 37 C.F.R. §§ 1.115 AND 1.121**

Sir or Madam:

Applicants submit the following Amendment and Remarks and respectfully request that the Examiner enter the Amendment and consider the Remarks prior to examination of the application. Amendments begin on page 2 of this paper. Remarks begin on page 6 of this paper.

Kindly enter the following Amendment and consider the following Remarks.